CA DESIGN ANNOUNCES INSTRUCTOR-LED PCB DESIGN TRAINING FOR HEWLETT-PACKARD

Official PCB Layout Training Company for HP Worldwide, CA Design is Conducting Courses on Allegro PCB Layout and Constraint Management for HP employees March 9-12th in San Diego, CA.

San Jose, CA (January 26, 2010) – CA Design announces that founder Robert Chandler, a twenty-plus year PCB Design veteran, will be instructing two courses for Hewlett-Packard engineers in Allegro PCB Layout and Constraint Management to be held in San Diego, California, March 9-12, 2010.

Allegro PCB Layout (Editor) will be the focus of three days of instruction with Constraint Management (Electrical Constraints) as the subject for the final day of training. The courses are intended for HP design engineers, PCB designers, PCB layout designers, managers, and others interested in learning about using a constraint-driven system to produce simple to complex designs in a scalable platform.

Instruction will be given from an end user's point-of-view and will take attendees through the entire Cadence® Allegro® PCB Editor process - from setting up designs to libraries and processes - all while teaching short-cuts and offering helpful hints along the way. The fourth day of training will concentrate on Constraint Management and encompass everything from setting up a board file for CMS topics to using specific and relative propagation delays.

Cadence® has this to say about their PCB Design product: "With its comprehensive feature set, Allegro® PCB Design offers the leading physical and electrical constraint-driven PCB layout and interconnect routing system. The fully integrated design flow includes design creation, library creation, placement, interactive routing and editing, automatic routing, and interfaces for manufacturing and mechanical CAD. The user interface is intuitive, easy-to-use, and consistent throughout the design flow. Large, dense PCB designs with high-speed interfaces can utilize Global Routing Environment technology for intelligent interconnect planning and routing automation..."

CA Design offers similar courses that are open to the public, including training in Concept HDL, Constraint Manager, Allegro PCB Editor, and Allegro Layout for Engineers. To learn more about CA Design's world-class on-site and web-based training programs, as well as its renowned PCB Layout service, please visit the company's website by clicking here.

About the Instructor:
Bob Chandler received an MBA in operations management from San Jose State University and a BS in business management from San Jose State University. Chandler has been a PCB designer for more than 20 years and has been using OrCAD Capture for schematics for 20 years. He has managed designers and engineers at TRW, Lockheed, CADCOR, PCA Design, Apple Computer, CA Design and SET Engineering. He has been a project manager, responsible for budgets and scheduling projects at TRW, PCA Design, Lockheed, Apple Computer, SET Engineering and is currently operating CA Design. CA Design is the official PCB design training (using Cadence Allegro layout software) company of HP Worldwide.

More About **CA DESIGN**:
Chandler Automated Design was founded in 1988 by Robert Chandler as a high-quality, low-cost source for Printed Circuit Board design, fabrication and assembly, training, and project management. CA Design produces premium designs for digital boards, small analog boards, ECL, RF, high-speed, surface mount, chip on board, and micro-via and specializes in various software platforms including, Altium® DX Designer®, Cadence® OrCAD®, Mentor Board Station, Cadence® Allegro® PCB Editor, PADS Power PCB, Protel, PCAD, Cadstar, and Mentor Expedition. CA Design's experienced designers, personalized service, and premium designs are the reason so many customers have become repeat clients over the past twenty-plus years. CA Design stakes their reputation on the quality of their work, training, and service.

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